



FIG. 1 (PRIOR ART)

Number of BIP violations	Bit 1	Bit 2	Bit 3	Bit 4
0	1	0	0	1
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
Incoming AIS	1	1	1	0

FIG. 2 (PRIOR ART)

